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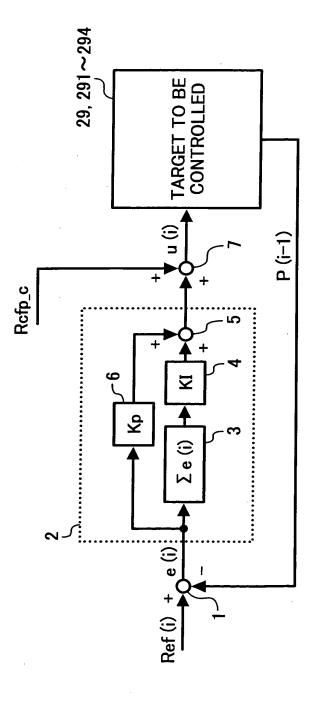
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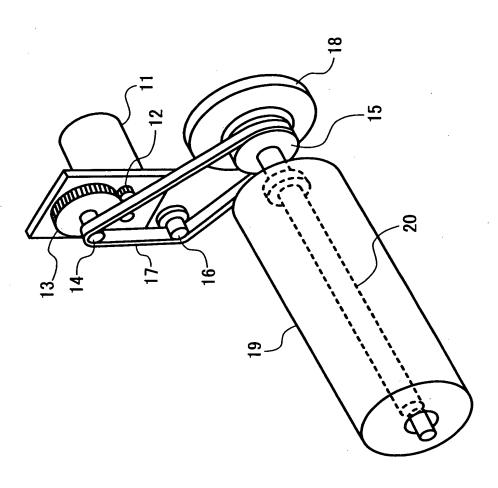
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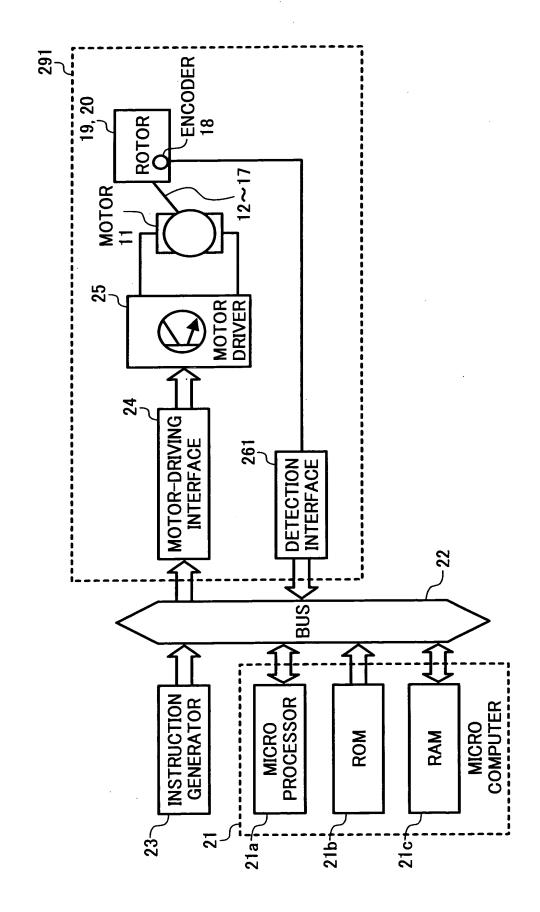
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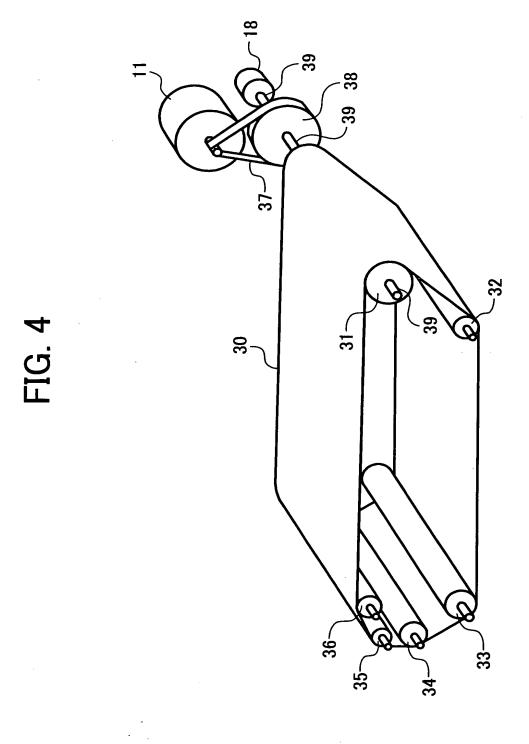








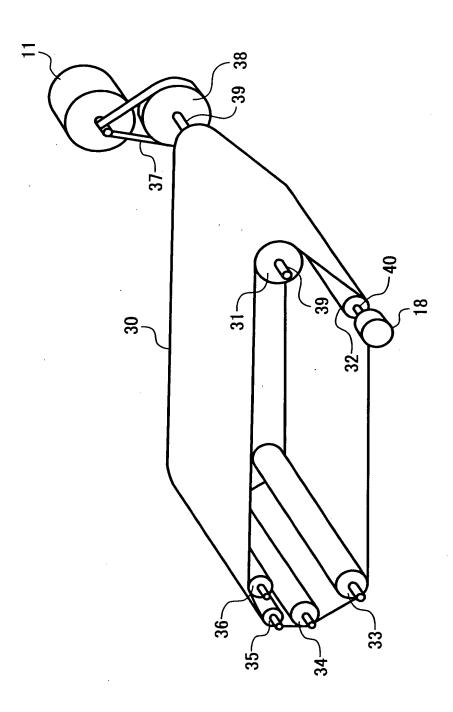


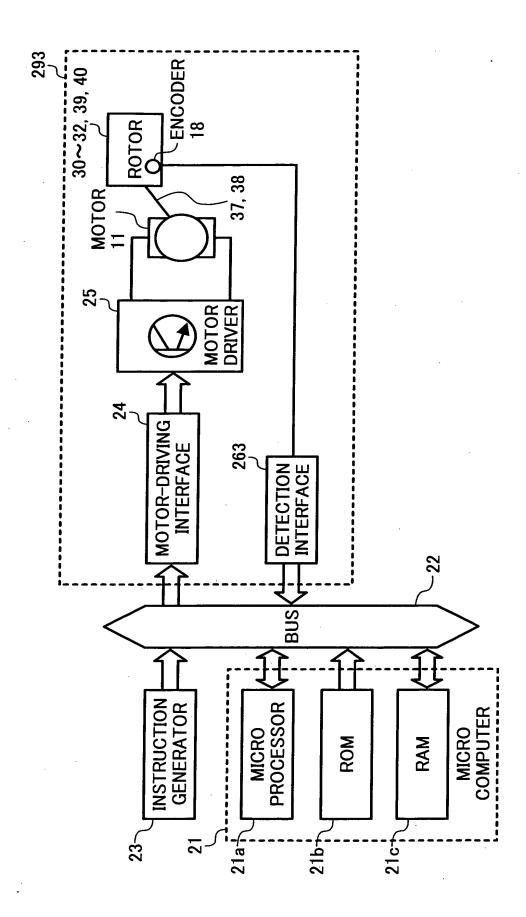


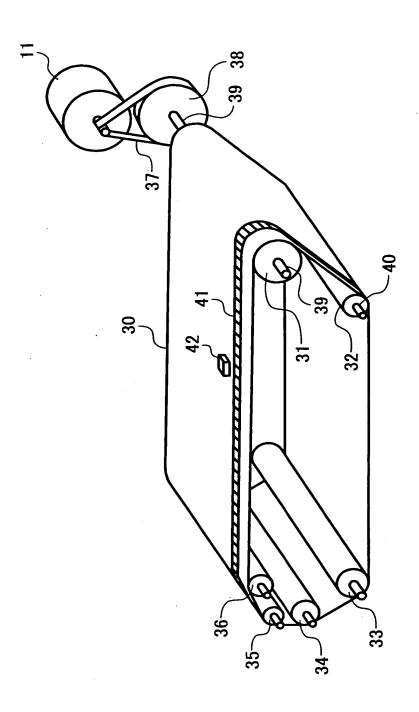
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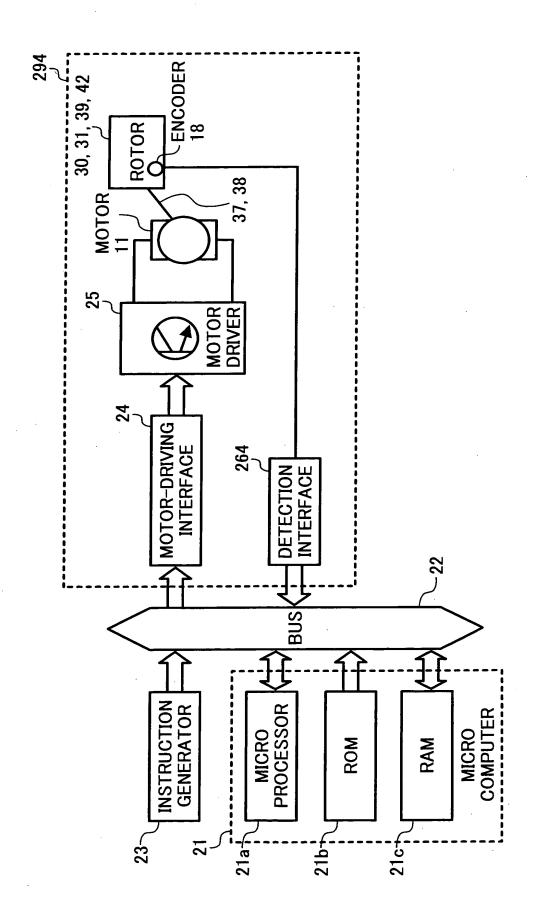
-ENCODER 18 30, 31, 39 ROTOR 37, 38 MOTOR 25 MOTOR DRIVER MOTOR-DRIVING INTERFACE 262 INTERFACE BUS NSTRUCTION MICRO COMPUTER **PROCESSOR** GENERATOR MICRO RAM ROM 21a+ 216+ 21c+

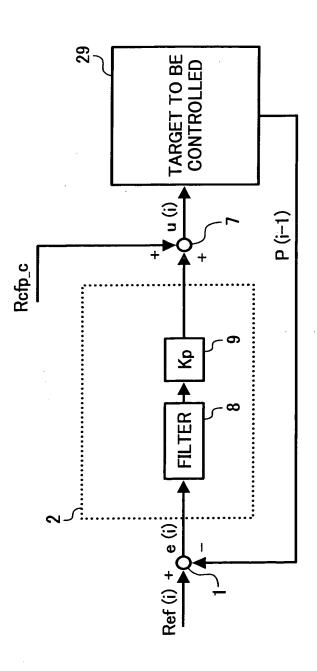
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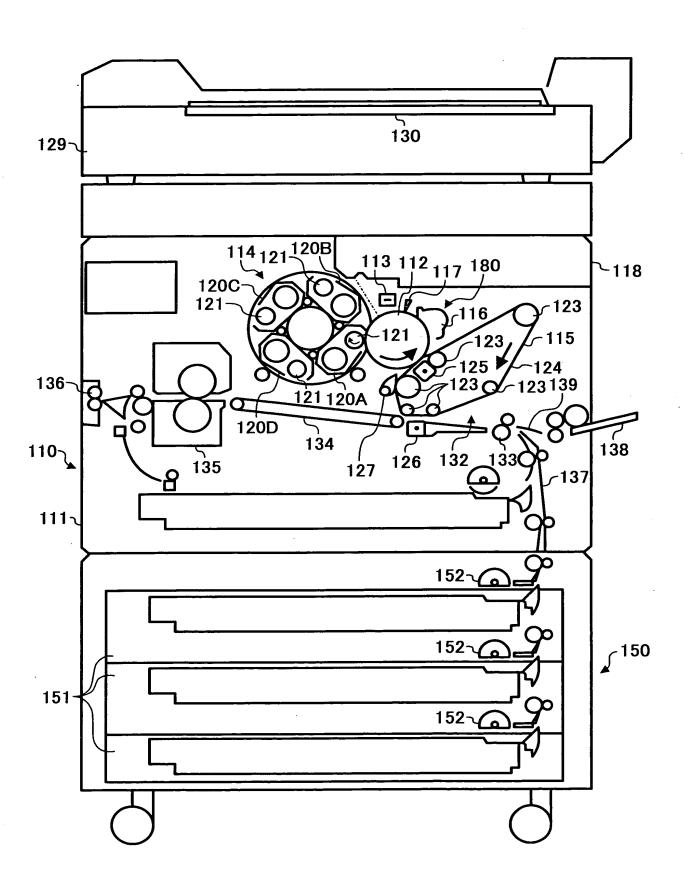


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FIG. 11



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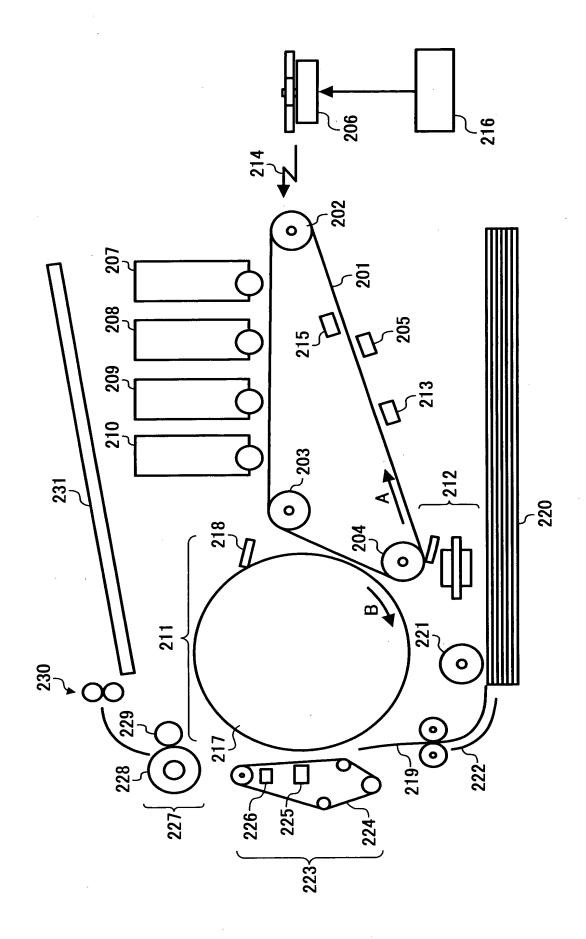
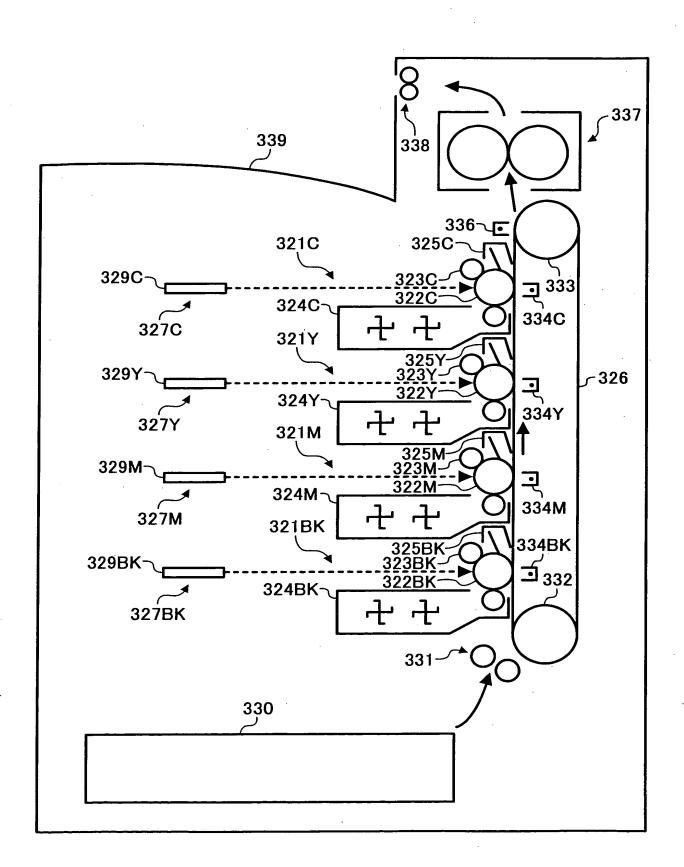
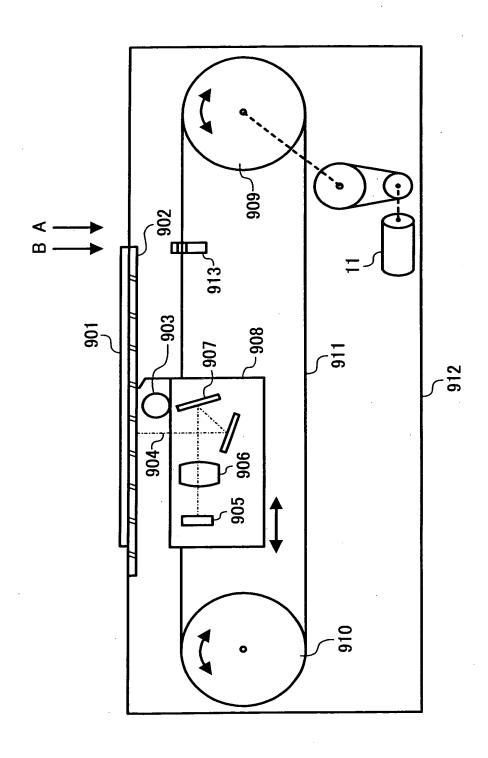


FIG. 12

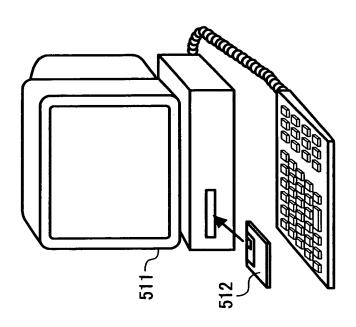
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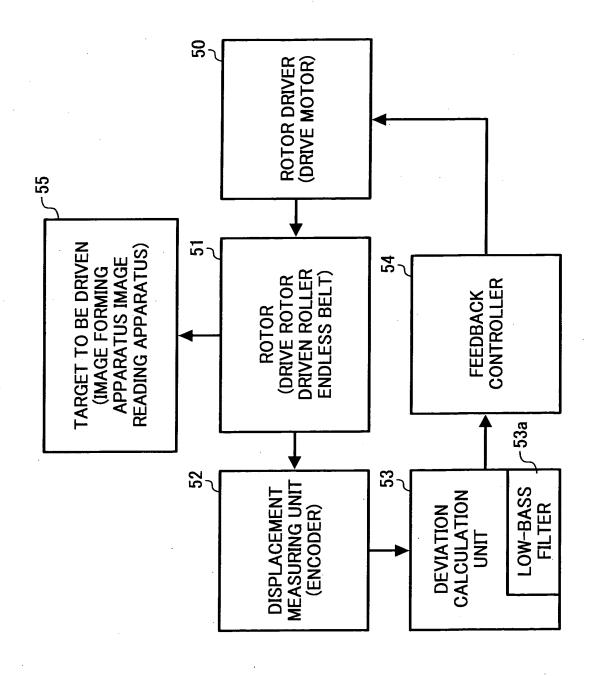
FIG. 13











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FIG. 17

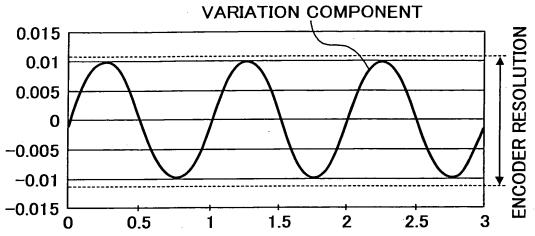


FIG. 18

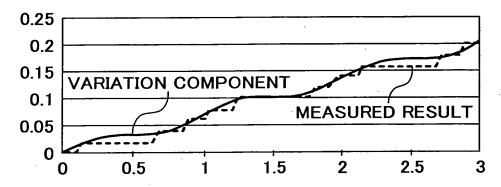


FIG. 19

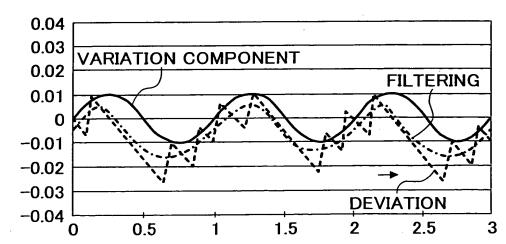
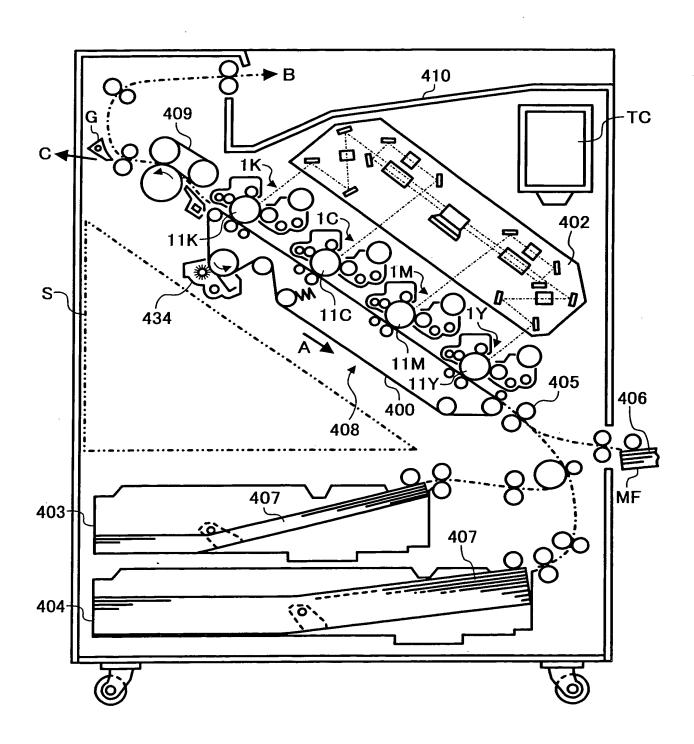


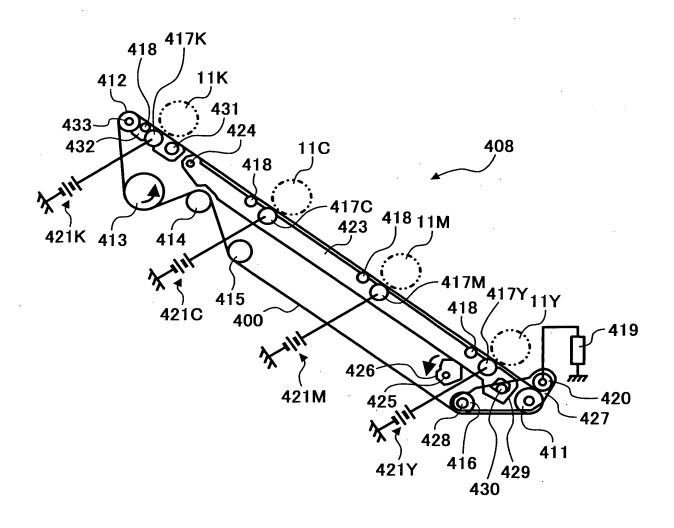
FIG. 20



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FIG. 21



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FIG. 22

